

#10/B
2.13.98
J.W.M.

JHM

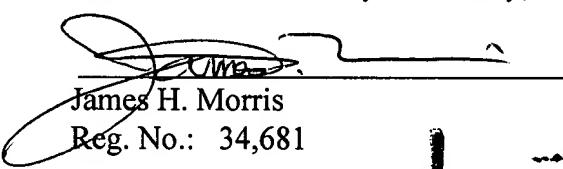
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Serge PONTAROLLO
Serial No.: 08/653,958
Filed: May 22, 1996
For: CLAMP

Examiner: LEJA, R.
Art Unit: 2104

CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)

The undersigned hereby certifies that this document is being placed in the United States mail with first-class postage attached, addressed to Box Fee Amendment, Assistant Commissioner for Patents, Washington, D.C. 20231, on the 29th day of January, 1998.


James H. Morris
Reg. No.: 34,681

Box Fee Amendment
Assistant Commissioner For Patents
Washington, D.C. 20231

RECEIVED

FEB 12 1998

GROUP 2100

Sir:

AMENDMENT

In response to the Office Action mailed September 29, 1997, please amend the above-identified application as follows:

IN THE CLAIMS

Please amend claims 1, 7, 11, 15, 18, 23, and 26 as follows:

Please cancel claim 26.

Subj.
(Once Amended) A device for protecting a circuit against voltage surges, comprising:

a MOS transistor of a first type directly connected to first and second supply terminals by its source and its drain, respectively;

a MOS transistor of a second type directly connected between the second supply terminal and the gate of the transistor of the first type by its source and its drain, respectively; and

B